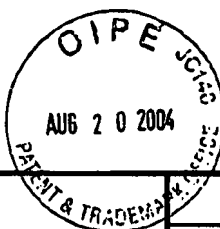


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10/667,951

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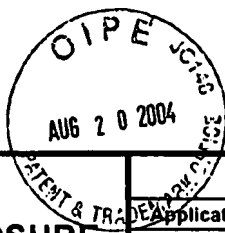
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		Filing Date	Sep 22, 2003
		First Named Inventor	Johns
		Group Art Unit	2183
		Examiner Name	
Sheet 2 of 2	Attorney Docket Number	MS1-1705US	

NON PATENT LITERATURE DOCUMENTS			
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/L.C./		AMMAR, et al.; "Graphic Modelling Technique For Software Execution Time Estimation"; Information and Software Technology, 1991, Vol. 33, No. 2; March; pp. 151-156.	
/L.C./		IKEDO; "Design and Performance Evaluation of a Pixel Cache Implemented Within Application-Specific Integrated Circuits"; Visual Computer, 1996, Vol. 12, No. 5, pp. 215-233.	
/L.C./		DOING; "Hard-Tuning the PS 2", Slides presented at Game Developers Conference Europe 2002, August 27-29, 2002; 36 pages.	
/L.C./		LEMARIE; "Performance Analyser Play Your Game Cycle by Cycle"; Slides presented at Australian Game Developer Conference 2002, December 7, 2002; 36 pages.	
/L.C./		"Performance Analyzer + Real World Examples" printed September 16, 2003 from Sony Computer Entertainment Europe Technology Group web site (www.scee.sony.co.uk/sceesite/articles.html, PerformanceAnalyzer.pdf), 4 pages.	
/L.C./		BENDER, et al.; "Introduction to the Performance Analyzer for PlayStation2"; Game Developers Conference, March 6-8, 2003; 57 pages.	

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